

SIMD Within A Register (SWAR)

- vector speedup for floating point
- AMD 3DNow!, Intel SSE, Moto Altivec
- we have developed compiler support
- can minimize precision loss

E.g., KLAT2's ScaLAPACK Rmax:

23 GFLOPS double-precision

28 GFLOPS single-precision

58 GFLOPS 3DNow!

