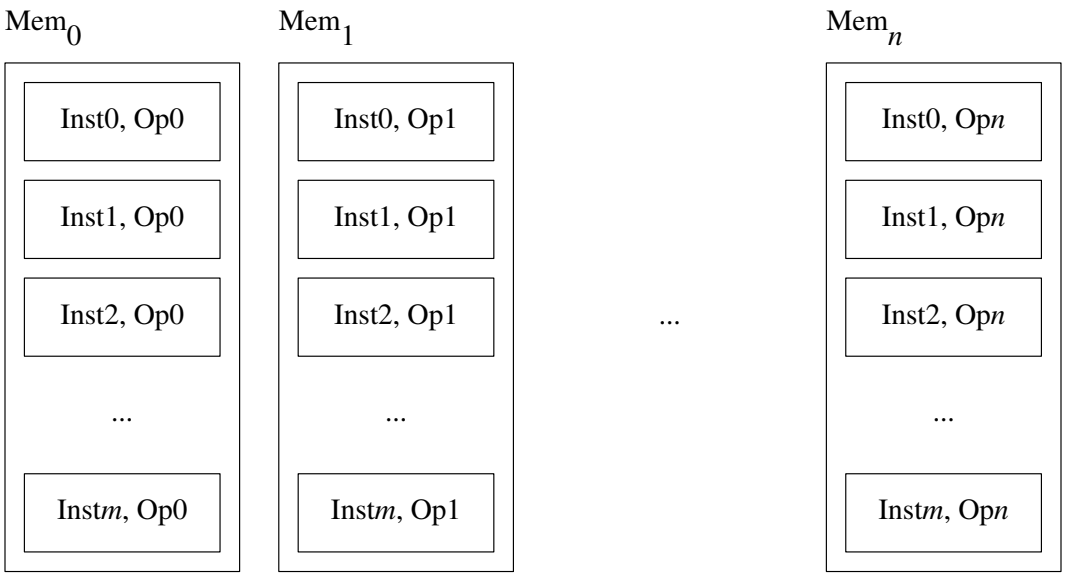


**Figure 2:** Memory Layout For VLIW Execution