

Reference Information

Feel free to separate (and discard) this sheet from your exam.
It is intended as a reference only.

MIPS Instructions (a subset)

| | |
|-------------------|------------------------------|
| <code>add</code> | <code>r1,r2,r3</code> |
| <code>addi</code> | <code>r1,r2,immediate</code> |
| <code>and</code> | <code>r1,r2,r3</code> |
| <code>andi</code> | <code>r1,r2,immediate</code> |
| <code>or</code> | <code>r1,r2,r3</code> |
| <code>ori</code> | <code>r1,r2,immediate</code> |
| <code>sub</code> | <code>r1,r2,r3</code> |
| <code>subi</code> | <code>r1,r2,immediate</code> |
| <code>xor</code> | <code>r1,r2,r3</code> |
| <code>xori</code> | <code>r1,r2,immediate</code> |
| <code>lui</code> | <code>r1,immediate</code> |
| <code>slt</code> | <code>r1,r2,r3</code> |
| <code>beq</code> | <code>r1,r2,label</code> |
| <code>bne</code> | <code>r1,r2,label</code> |
| <code>j</code> | <code>label</code> |
| <code>jal</code> | <code>label</code> |
| <code>jr</code> | <code>r1</code> |
| <code>lw</code> | <code>r1,offset(r2)</code> |
| <code>sw</code> | <code>r1,offset(r2)</code> |

Single-Cycle Implementation Architecture

